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APPLICANTS

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** CONTINUING DATA *****

M NONE

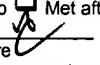
** FOREIGN APPLICATIONS *****

JAPAN 2001-092892 03/28/2001

M YES

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 31	INDEPENDENT CLAIMS 3
Verified and Acknowledged	Examiner's Signature  Initials				

ADDRESS

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TITLE

Delay circuit, semiconductor integrated circuit device containing a delay circuit and delay method

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